

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF: U.S. Patent No. 7,346,755

USPTO CONFIRMATION CODE: 4003

APPLICATION NO.: 10/663,128

FILED: Sep. 16, 2003

EXAMINER: Stephen C. Elmore

GROUP ART UNIT: 2185

FOR: MEMORY QUALITY ASSURANCE

ATTORNEY DOCKET NUMBER: 200308565-1

37 CFR 1.322 & 37 CFR 1.323 REQUEST FOR CERTIFICATE OF CORRECTION

HONORABLE COMMISSIONER OF PATENTS & TRADEMARKS

Sir:

The following is a request for a certificate of correction in Serial Number 10/663,128, now Patent Number 7,346,755.

A certificate of correction under 35 USC 254 is respectfully requested in the above-identified patent:

☒ All errors were the fault of the USPTO, no fee required. In the event that a further fee is required, please charge the amount to Deposit Account No. 082025.

☐ All errors were the fault of the applicant and, accordingly, please charge \$100.00 to our Deposit Account No. 082025. In the event that a further fee is required, please charge the amount to the same Deposit Account.

☐ The errors were the fault of both the applicant and USPTO and, accordingly, please charge \$100.00 to our Deposit Account No. 082025. In the event that a further fee is required, please charge the amount to the same Deposit Account.

The exact locations where the errors appear in the patent and patent application are as follows:

On the face page, in field (57), under "Abstract", lines 1-15, delete "An example memory quality assuring system is provided. The system may include a memory mapping logic configured to facilitate accessing memory locations and redirecting memory accessing operations. The system may also include a memory quality assurance logic configured to logically replace a first memory location with a second memory location, to initiate testing logically isolated memory locations, and to selectively logically remove tested memory locations based on the testing.

It is emphasized that this abstract is provided to comply with the rules requiring an abstract that will allow a searcher or other reader to quickly ascertain the subject matter of the application. It is submitted with the understanding that it will not be employed to interpret or limit the scope or meaning of the claims 37 CFR 1.72(b)." and insert - - Methodology, systems, and media associated with memory quality assurance are described. One exemplary method embodiment comprises selectively copying the contents of a first memory location to a second memory location, logically replacing the first memory location with the second memory location and initiating memory testing of the first memory location without an operating system interaction. - -, therefor.

(Preliminary Amendment to abstract filed on Oct. 02, 2003, page 1, lines 11-15)

In column 16, line 56, delete "A. Garner, A Dictionary of Modem" and insert - - A. Garner, A Dictionary of Modem - -, therefor.

(Specification filed on Sep. 16, 2003, page 22, lines 21-22)

In column 17, line 29, in Claim 5, delete "first memory" and insert - - memory - -, therefor.

(Amendment to Claims filed on Aug. 03, 2007, page 3, in Claim 5, line 2)

In column 17, line 43, in Claim 7, delete "pad," and insert - - part, - -, therefor.

(Amendment to Claims filed on Aug. 03, 2007, page 3, in Claim 7, line 4)

In column 17, line 53, in Claim 9, delete "loge," and insert - - logic, - -, therefor.

(Amendment to Claims filed on Aug. 03, 2007, page 4, in Claim 9, line 1)

In column 18, line 67, in Claim 27, after "zero test" insert - - , - -.

(Amendment to Claims filed on Aug. 03, 2007, page 6, in Claim 27, line 3)

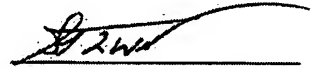
In column 19, line 28, in Claim 29, after "of memory" delete "." and insert - - , - - ,
therefor.

(Amendment to Claims filed on Aug. 03, 2007, page 7, in Claim 29, line 6)

The requested corrections are attached on Form PTO 1050.

Respectfully Submitted

5/14/2008
DATE


Name: Steven L. Webb
Registration No.: 44,395

Attorney/Agent of Record

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 2

PATENT NO : 7,346,755
APPLICATION NO. : 10/663,128
ISSUE DATE : Mar. 18, 2008
INVENTOR(S) : Ken Gary Pomaranski, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (57), under "Abstract", lines 1-15, delete "An example memory quality assuring system is provided. The system may include a memory mapping logic configured to facilitate accessing memory locations and redirecting memory accessing operations. The system may also include a memory quality assurance logic configured to logically replace a first memory location with a second memory location, to initiate testing logically isolated memory locations, and to selectively logically remove tested memory locations based on the testing.

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(Preliminary Amendment to abstract filed on Oct. 02, 2003, page 1, lines 11-15)

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Intellectual Property Administration
P. O. Box 272400
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This collection of information is required by 37 CFR 1.322, and 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patent, P.O. Box 1450, Alexandria, VA 22313-1450.

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Page 2 of 2

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